

Abstract of the Disclosure:

A circuit configuration has an integrated semiconductor element, preferably, an intelligent power semiconductor, and a display element. A display element diagnoses and displays
5 disturbances in the integrated semiconductor element. The display element connects electrically firstly to the diagnostic output of the semiconductor element and secondly to the load output of the power stage, and contains light-emitting semiconductor diodes. The light-emitting diodes are
10 connected in antiparallel and output to their environment visual information items and can be distinguished in accordance with the current fed to them.

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